



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/632,273
Filing Date July 31, 2003
Inventor..... Warren M. Farnworth, et al.
Assignee Micron Technology, Inc.
Group Art Unit..... 2829
Examiner Russell M. Kobert
Attorney's Docket No. MI22-2379
Title: Method and Apparatus for Testing Semiconductor Circuitry for Operability and
Method of Forming Apparatus for Testing Semiconductor Circuitry for Operability

**RESPONSE TO DECEMBER 23, 2005 NOTICE OF ALLOWANCE
TO ACCOMPANY REQUEST FOR CONTINUED EXAMINATION (RCE)**

To: Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

From: D. Brent Kenady
Tel. 509-624-4276; Fax 509-838-3424
Wells St. John P.S.
601 West First Avenue, Suite 1300
Spokane, WA 99201-3828

Sir:

Responsive to the Notice of Allowance dated November 4, 2005, Applicant amends
and remarks as follows:

AMENDMENTS

Underlines indicate insertions and ~~strikeouts~~ indicate deletions.